

# EPROM emulator protocol

## 14 June 2005

### CONVENTIONS:

'X' ==> Ascii char 'X'  
0x12 ==> Hex value  
12 ==> Decimal value

### CHECKSUM:

2's complement of the sum of all the data bytes in the packet.  
Selected so that the sum of the entire packet = 0.

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### VERSION REQUEST:

ALL H/W AND F/W VERSIONS

'V'	CHK

CHK: Checksum

### RESPONSE:

ERROR	MAJOR	MINOR	HW ID

ERROR: Error code.  
MAJOR: Major version  
MINOR: Minor version  
HW ID: Hardware ID (0x4D = 512kbit)

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### GET ID STRING:

ALL H/W AND F/W VERSIONS

'G'	CHK

CHK: Checksum

### RESPONSE:

ERROR	LEN	IDSTR

ERROR: Error code. See list at end of document.  
LEN: Length of ID string  
IDSTR: ID string

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### BASIC WRITE BLOCK:

ALL H/W AND F/W VERSIONS

'W'	ADDRh	ADDRl	COUNT	DATA	CHK

COUNT: Number of DATA bytes - 1 (up to 255 if RTS/CTS used, max 15 if not)  
ADDRh: Base address high byte  
ADDRl: Base address low byte  
DATA: Data to write  
CHK: Checksum

### RESPONSE:

ERROR	COUNT

ERROR: Error code.  
0x00 = OK (rest of packet sent)  
0x01 = Checksum invalid (only result byte sent)  
COUNT: Number of DATA bytes written - 1

**SET MODE:**

ALL H/W AND F/W VERSIONS

```
| 'M' | MODE | DEVICE | CHK |  
|-----|-----|-----|-----|
```

MODE: Mode identifier  
0x00 = LOAD  
0x01 = RUN

DEVICE: Device code  
0x00 = 2764 \*  
0x01 = 27128  
0x02 = 27256  
0x03 = 27512

CHK: Checksum

\* 2764 = 27128, but data must be loaded twice (as if data had been appended to itself) if a 2764 is being emulated.

**RESPONSE:**

```
| ERROR |  
|-----|
```

ERROR: Error code.

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**Command 0x00: NOP (ignored, no response). Can be used to re-synchronize the communications link.**

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**ERROR CODES:**

0x00 = OK	(complete packet sent)	
0x01 = Checksum invalid	(only result byte sent)	[ ANY ]
0x02 = Unsupported command	(only result byte sent)	[ ANY ]
0x03 = Invalid mode identifier	(only result byte sent)	[ 'SET MODE' ONLY ]
0x04 = Invalid device code	(only result byte sent)	[ 'SET MODE' ONLY ]